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# NOTICE OF ALLOWANCE AND FEE(S) DUE

47795

7590

11/12/2009

TROP, PRUNER & HU, P.C. 1616 S. VOSS RD., SUITE 750 HOUSTON, TX 77057-2631

EXAMINER

KHATRI, ANIL

ART UNIT PAPER NUMBER

2191

DATE MAILED: 11/12/2009

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/563,313	12/29/2005	Sergey N. Zheltov	ITL.2174US (P21486)	6943

TITLE OF INVENTION: PLATFORM INDEPENDENT BINARY INSTRUMENTATION METHOD

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0	\$1810	02/12/2010

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

#### HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

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III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

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			[						(Depositor's name)
									(Signature)
									(Date)
APPLICATION NO.	FILING DATE		FIRST NAMED INVENT	OR		ATTO	RNEY DOCKET NO.	CONF	IRMATION NO.
10/563,313 TITLE OF INVENTION	12/29/2005 I: PLATFORM INDEPE	NDENT BINARY INSTI	Sergey N. Zheltov RUMENTATION MET	HOD	)	ITL.	2174US (P21486)		6943
APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DU	ЛЕ I	PREV. PAID ISSUE	FEE	TOTAL FEE(S) DUE		DATE DUE
nonprovisional	NO	\$1510	\$300		\$0	\$1810			02/12/2010
EXAM	IINER	ART UNIT	CLASS-SUBCLASS	$\neg$					
KHATRI, ANIL 2191			717-158000						
"Fee Address" ind PTO/SB/47; Rev 03-0 Number is required.  3. ASSIGNEE NAME A PLEASE NOTE: Un	ND RESIDENCE DATA less an assignee is identi h in 37 CFR 3.11. Comp	" Indication form	data will appear on the	nativel ngle i or age attorne be pr type type	ly, firm (having as a sent) and the name eys or agents. If n rinted. ) ent. If an assigne signment.	memb s of u o nam	er a 2 p to lee is 3 lentified below, the de	ocument	has been filed for
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NOTE: The Issue Fee an	ns SMALL ENTITY statu	us. See 37 CFR 1.27.	b. Applicant is no ld from anyone other tha	_	_				
interest as shown by the	records of the United Sta	tes Patent and Trademark	Office.						
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10/563,313	12/29/2005	Sergey N. Zheltov	1TL.2174US (P21486)	6943			
47795 75	47795 7590 11/12/2009			EXAMINER			
TROP, PRUNER	. & HU, P.C.		KHATRI, ANIL				
1616 S. VOSS RD., SUITE 750			ART UNIT	PAPER NUMBER			
HOUSTON, TX 7	7057-2631		2191				
		DATE MAILED: 11/12/2009					

# Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 803 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 803 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

	Application No.	Applicant(s)			
	10/563,313	ZHELTOV ET AL.			
Notice of Allowability	Examiner	Art Unit			
	Anil Khatri	2191			
The MAILING DATE of this communication appear All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R	(OR REMAINS) CLOSED in this app or other appropriate communication GHTS. This application is subject to	olication. If not included will be mailed in due course. <b>THIS</b>			
1. This communication is responsive to <u>8/6/09</u> .					
2. The allowed claim(s) is/are <u>1, 3-11 and 13-30</u> .					
3. ☐ Acknowledgment is made of a claim for foreign priority ur  a) ☐ All b) ☐ Some* c) ☐ None of the:  1. ☐ Certified copies of the priority documents have 2. ☐ Certified copies of the priority documents have 3. ☐ Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)).  * Certified copies not received:	been received. been received in Application No cuments have been received in this	national stage application from the			
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.	IENT of this application.				
<ol> <li>A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give</li> </ol>					
<ol> <li>CORRECTED DRAWINGS ( as "replacement sheets") must</li> <li>(a) ☐ including changes required by the Notice of Draftspers</li> <li>1) ☐ hereto or 2) ☐ to Paper No./Mail Date</li> <li>(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date</li> <li>Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in the</li> </ol>	on's Patent Drawing Review (PTO- s Amendment / Comment or in the C	office action of			
6. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT					
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☑ Notice of Draftperson's Patent Drawing Review (PTO-948)	5. ☐ Notice of Informal P 6. ☐ Interview Summary	• •			
Paper No./Mail Date  Information Disclosure Statements (PTO/SB/08),  7. ☑ Examiner's Amendment/Comment					
Paper No./Mail Date  4.  Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. ☑ Examiner's Stateme	ent of Reasons for Allowance			

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Jack Richard, registration no.53, 514 on 10/23/09.

The application has been amended as follows:

In Claims

Cancel claims 2 and 12.

Claim1: A method of binary instrumentation comprising:

using a processor, coupled to a memory, to allocate a contiguous memory region included in the memory, wherein a total number of original functions to be intercepted is unknown at the time of the allocation;

filling the memory region with (A) first and second platform independent high level non-assembly language copies of an interceptor function (B) but not any version of the original functions;

initializing a first data structure with at least a starting address, length of the allocated memory region, and a reference to a second data structure;

storing an address of one of the original functions to be intercepted in a current element of the second data structure upon request for instrumentation; and

providing a starting address of the second copy of the interceptor function upon request for instrumentation;

wherein instructions for allocating the memory region, filling the memory region, and initializing the first data structure are executed upon initial request for instrumentation so that each time an additional original function needs to be intercepted a new copy of the interceptor function is associated with the additional original function.

#### Claim 11

Last line, after instrumentation delete - - .- -

Last line, after instrumentation insert - - ; wherein instructions for allocating the memory region, filling the memory region, and initializing the first data structure are executed upon initial request for instrumentation so that each time an additional original function needs to be intercepted a new copy of the interceptor function is associated with the additional original function.

Claim 21: A system that performs binary instrumentation, comprising:
a processor, coupled to a memory, to allocate a contiguous memory region
included in the memory, wherein a total number of original functions to be intercepted is
unknown at the time of the allocation; to fill said memory region with (A) first and second
platform independent high level non-assembly language copies of an interceptor

function (B) but not any version of the original functions; to initialize a first data structure with at least a starting address, length of the allocated memory region, and a reference to a second data structure; to store an address of one of the original functions to be intercepted in a current element of the second data structure upon request for instrumentation; and to provide a starting address of the second copy of the interceptor function upon request for instrumentation;

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wherein instructions for allocating the memory region, filling the memory region, and initializing the first data structure are executed upon initial request for instrumentation so that each time an additional original function needs to be intercepted a new copy of the interceptor function is associated with the additional original function.

Claim 22: The system of claim 21, wherein each one of the copies is not created until a corresponding request for instrumentation occurs such that the system is configured to dynamically adapt to the number of original functions to be instrumented.

Claim 23: The system of claim 21, wherein the processor is to allocate the memory region, fill the memory region, and initialize the first data structure only if all interceptor function copies of currently allocated memory regions are associated with previous requests for instrumentation.

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Claim 24: The system of claim 21, wherein the processor is to duplicate the first data structure to associate each new copy of the first data structure with each newly allocated memory region.

Claim 26: The system of claim 21, wherein the processor is to maintain the current element of the second data structure to establish a correspondence between one of the original functions and a provided address of one of the interceptor function copies.

Claim 27: The system of claim 26, wherein the processor is to select a next successive element of the second data structure as the current element for each new request for instrumentation and the copies are stored adjacent to one another.

Claim 30: The system of claim 21, wherein the processor is to: obtain an address being currently executed;

retrieve from a corresponding copy of the first data structure the starting address of a memory region that contains the address being currently executed;

fetch the reference to the second data structure from said copy of the first data structure;

compute an index to the second data structure as the fetched reference to the second data structure added to the difference between the address being currently

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executed and the retrieved starting address, said difference divided by the size of the interceptor function; and

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read, from the second data structure indexed with the computed index, the address of an original function to pass control to.

### Allowable Subject Matter

Claims 1, 3-11 and 13-30 (renumbered 1- 28) are allowed.

The following is an examiner's statement of reasons for allowance:

The cited prior art taken alone or in combination does not teach or fairly suggest the claimed invention of binary code instrumentation and, more specifically, to designing platform-independent binary instrumentation. The cited prior art teaches computer memory management and performing time measurements during instrumentation-based profiling but silent on allocating a contiguous memory region included in the memory, wherein a total number of original functions to be intercepted is unknown at the time of the allocation and filling the memory region, and initializing the first data structure are executed upon initial request for instrumentation so that each time an additional original function needs to be intercepted a new copy of the interceptor function is associated with the additional original function. Therefore, it improves on eliminating the computational complexity normally associated with the processing of source codes, facilitates the creation of universal instrumentation and analysis systems, and enables dynamic instrumentation to be performed at runtime.

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Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

#### Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Anil Khatri whose telephone number is 571-272-3725. The examiner can normally be reached on M-F 8:30-5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wei Zhen can be reached on 571-272-3708. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information

system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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/Anil Khatri/

Primary Examiner, Art Unit 2191